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PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Pechanek et al.

Serial No.: 09/422,015

Filed: October 21, 1999

For: METHODS AND APPARATUS FOR ABBREVIATED INSTRUCTION SETS  
ADAPTABLE TO CONFIGURABLE PROCESSOR ARCHITECTURES  
(as amended)

Group: 2183

Examiner: K. Kim

Chapel Hill, North Carolina  
September 4, 2001Assistant Commissioner For Patents  
Washington, DC 20231Supplemental Amendment

Please amend the above identified application as follows:

In the TitlePlease replace the original title with a new one reading --METHODS AND  
APPARATUS FOR ABBREVIATED INSTRUCTION SETS ADAPTABLE TO  
CONFIGURABLE PROCESSOR ARCHITECTURES--.In the Claims

Please amend claims 1, 18, 26 and 49 as follows:

1. (twice amended) A method for generating an abbreviated application specific  
program utilizing an abbreviated instruction set comprising the steps of: